	Application No.	Applicant(s)	W.
Notice of Allowability	10/790,276	YAMADA ET AL.	
	Examiner	Art Unit	
	Albert W Paladini	2125	
The MAILING DATE of this communication appear All claims being allowable, PROSECUTION ON THE MERITS IS (herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT Report of the Office or upon petition by the applicant. See 37 CFR 1.313	OR REMAINS) CLOSED in this or other appropriate communica GHTS. This application is subje	application. If not include ition will be mailed in due	course. THIS
1. This communication is responsive to application filed on 2/2	<u>27/04</u> .		
2. The allowed claim(s) is/are 11-21.			
3. $\boxtimes$ The drawings filed on <u>27 February 2004</u> are accepted by the	ne Examiner.		
4.  Acknowledgment is made of a claim for foreign priority unall All b)  Some* c) None of the:  1.  Certified copies of the priority documents have 2.  Certified copies of the priority documents have 3.  Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)).  * Certified copies not received:  Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.  5.  A SUBSTITUTE OATH OR DECLARATION must be subminsformal PATENT APPLICATION (PTO-152) which give 6.  CORRECTED DRAWINGS (as "replacement sheets") must be considered by the Notice of Draftspers 1) hereto or 2) to Paper No./Mail Date  (b) including changes required by the attached Examiner' Paper-No./Mail-Date	der 35 U.S.C. § 119(a)-(d) or (f) been received. been received in Application No cuments have been received in the of this communication to file a re MENT of this application.  witted. Note the attached EXAMII es reason(s) why the oath or deceived in the st be submitted. son's Patent Drawing Review ( File. s Amendment / Comment or in the	this national stage applicately complying with the respect to the complying with the respect to the complying with the respect to the control of the Office action of	quirements NOTICE OF
Identifying indicia such as the application number (see 37 CFR and sheet. Replacement sheet(s) should be labeled as such in	the header according to 37 CFR 1	.121(d).	
<ol> <li>DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT</li> </ol>	osit of BIOLOGICAL MATERI FOR THE DEPOSIT OF BIOLO	IAL must be submitted. DGICAL MATERIAL.	Note the
<ul> <li>Attachment(s)</li> <li>1. ☑ Notice of References Cited (PTO-892)</li> <li>2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)</li> <li>3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/Paper No./Mail Date 3/1/04)</li> <li>4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material</li> </ul>	6. ☐ Interview Sum Paper No./Ma 08), 7. ☐ Examiner's An 8. ☑ Examiner's Sta 9. ☐ Other	all Date nendment/Comment atement of Reasons for Al	lowance
	À	LBERT W. PALADINI RIMARY EXAMINER	-( 1 O

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## Reasons for Allowance

1. The following is an examiner's statement of reasons for allowance: The reasons for allowance are the same reasons provided in the amendment for Application no. 10/082,247 (US Patent 6,711,453) on November 29, 2003. No new relevant references have been found.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

2. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Cheong (6055463) discloses a control system for a semiconductor test process, which includes yield analysis at the final test, and failure analysis and correction when the desired yield has not been achieved.

Hu (6314379) discloses a yield management system for a semiconductor device line, which includes inspection and quality control after each process step, computation of final test yield, reliability, and a feedback system for defect management based upon the testing and analysis.

Steffan (6338001) discloses a defect management for semiconductor devices wherein defects on inspected wafers are tabulated, and where a defect kill ratio is used as a criterion for the yield engineer to optimize the process.

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Mizuno (6542830) discloses a process management system for inspecting defects on a semiconductor wafer, classifying defects, performing yield prediction by chip, and modifying the process to minimize defects increase yield.

3. Any inquiry concerning this communication or earlier communication from the examiner should be direct to Albert W. Paladini whose telephone number is (703) 308-2005. The examiner can normally be reached from 7:30 to 3:30 PM on Monday, Tuesday, Thursday, and Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mr. Leo P. Picard, can be reached on (703) 308-0538. The official fax phone number for the organization where this application or proceeding is assigned is (703)

872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

September 14, 2004

Albert W. Paladini Primary Examiner Art Unit 2125

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